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ASA-1062

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

T. SASAKI et al

Serial No. 10/073,312

Group Art Unit: 2825

Filed: February 13, 2002

Examiner: M. Dimyan

For: DESIGN METHOD AND SYSTEM FOR
ACHIEVING A MINIMUM MACHINE
CYCLE FOR SEMICONDUCTOR INTEGRATED
CIRCUITS (AS AMENDED)

REPLY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 25, 2005

Sir:

In reply to the final rejection mailed August 25, 2004,
and further to the Notice of Appeal filed February 25, 2005,
please amend the above-identified application as follows.